

512MB (x64) 184-PIN UNBUFFERED DDR SDRAM DIMM

DDR SDRAM DIMM

D32PB12C2 - 512MB

For the latest data sheet, please visit the Super Talent Electronics web site: www.supertalentmemory.com

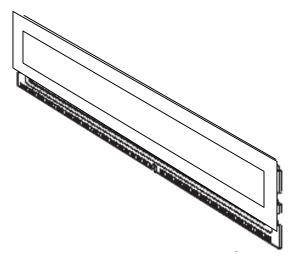
Features

- Power supply : Vdd: 2.6V ± 0.2V, Vddq: 2.6V ± 0.2V
- Double-data-rate architecture; two data transfers per clock cycle
- Bidirectional data strobe(DQS)
- Differential clock inputs(CK and CK)
- DLL aligns DQ and DQS transition with CK transition
- Programmable Read latency 2 (clock)
- Programmable Burst length (2, 4, 8)
- Programmable Burst type (sequential & interleave)
- Timing Reference: 2-3-3-7
- Edge aligned data output, center aligned data input
- Auto & Self refresh, 7.8us refresh interval(8K/64ms refresh)
- Serial presence detect with EEPROM
- High Performance Heat Spreader
- PCB : Height 1.250" (31.75mm), double sided component

Figure 1: Module with Heat Spreader

Description

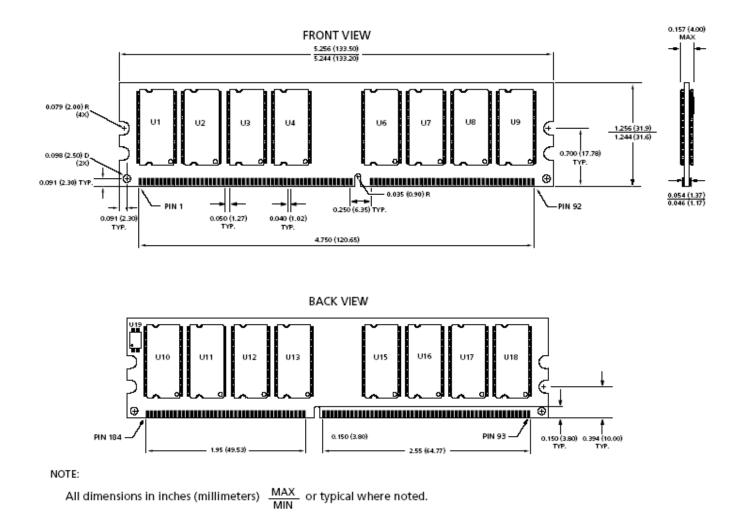
This document describes Super Talent Electronics' 64M x 64-bit (512MB) DDR400 SDRAM (Synchronous DRAM) CL2 memory module. The components on this module include sixteen 32M x 8-bit (8M x 8-bit x 4 Bank) DDR400 SDRAM in TSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V.



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